FR400 Series
Instruction Set Manual

Revision History

Version No.	Date of issue	Revision contents
Version 1.5	20/06/2003	
Version 2.0	22/12/2003	1.2.1, 1.2.3, 1.2.4, 1.2.7, 1.2.9 Add : MB93451 1.5.8 Correct : When ~~the processor is in the debug running mode and~~ the d field is 1, the RETT 1.7.1 Correct : When all of the bits as the result of XOR calculating are ~~"1" or~~ "0", the result of SCAN instruction is ~~0~~ 1. 1.9.13 Add : LRAI 1.9.14 Add : LRAD 1.9.15 Add : TLBPR 1.10.44 Add : MQLCLRHS 1.10.45 Add : MQLMTHS 1.10.46 Add : MQSLLHI, MQSRAHI 4 Add : MB93451
Version 2.1	15/01/2004	3 Add MB93451 Add LRAI, LRAD, TLBPR, MQLCLRHS, MQHCLPHS, MQSRAHI, MQSLLHI
Version 2.2	26/01/2004	3 Correct: MQHCLPHS -> MQLMTHS